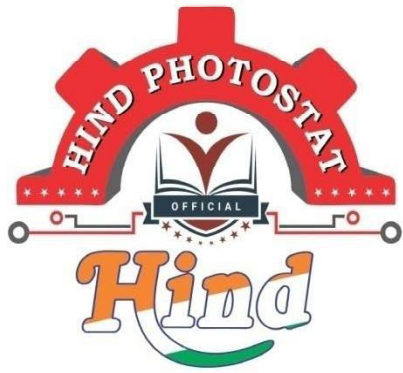




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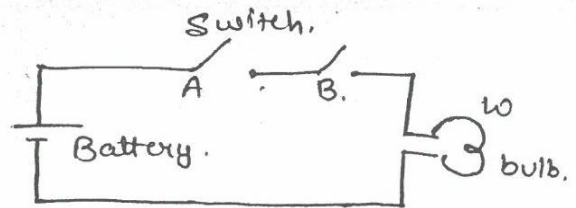
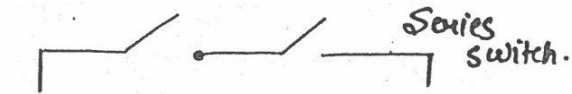
Boolean Algebra.

All variables $\rightarrow 5V$.
 $1 \rightarrow \text{high / True.}$
 $0 \rightarrow \text{low / False.}$
 $\rightarrow 0V / \text{Ground}$

Operators.

AND GATE

AB	W
0 0	0
0 1	0
1 0	0
1 1	1



("AND gate is Series Switch")

Truth table $\rightarrow$ Input/output Relationship.

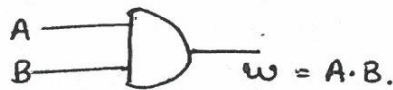
Ex: AND from Set Theory.

$X: \{2, 3, 4\}$

$Y: \{3, 5, 6\}$

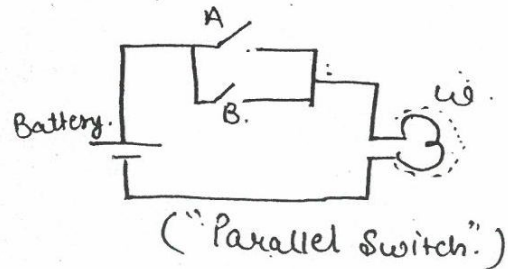
$(X \cap Y) = \{3\}$. [" $\cap$ " $\rightarrow$ AND].

And is generally
 $\cap$ intersection

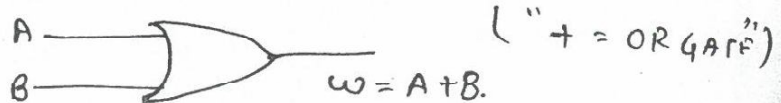


OR GATE

AB	W.
0 0	0
0 1	1
1 0	1
1 1	1



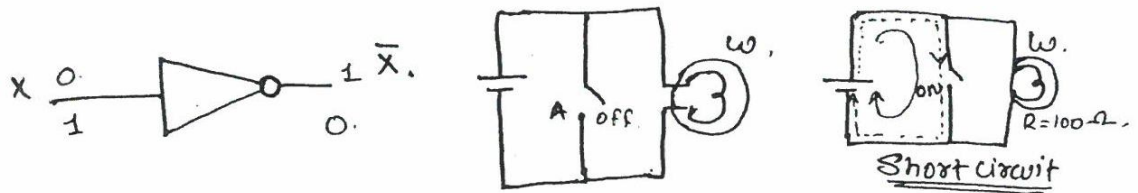
("Parallel Switch.")



From Set Theory.

$X \cup Y \rightarrow$ OR gate.

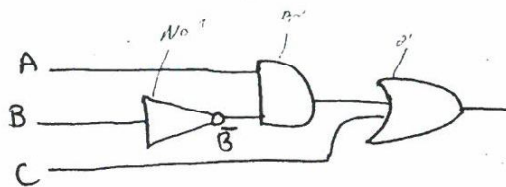
NOT Gate / Inverter.



["Current $\rightarrow 0$, Resistance $= \infty$ "]
 Current always flows from least resistance path.
 [$V = IR$].

- $\rightarrow$ AND, OR, NOT gate are called Basic gates.
 $\rightarrow$ Precedence of operators () > NOT > AND > OR

Example :- $Y = A \cdot \bar{B} + C$



1 $\rightarrow$ OR
 2 $\rightarrow$ AND.

1
 NOT
 AND
 OR

AND properties.

- 1.) $X \cdot 0 = 0$. 3.) $X \cdot X = X$.
 2.) $X \cdot 1 = X$ 4.) $X \cdot \bar{X} = 0$.

OR properties.

- 1.) $X + 0 = X$. 3.) $X + X = X$
 2.) $X + 1 = 1$. 4.) $X + \bar{X} = 1$.

Distributive Property :-

$$A \cdot (B + C) = A \cdot B + A \cdot C.$$

Ques Prove $(A+B) \cdot (A+C) = A + B \cdot C.$

Solⁿ $A \cdot (A+C) + B \cdot (A+C).$

$$\textcircled{A \cdot A} + A \cdot C + B \cdot A + B \cdot C.$$

$$\Rightarrow A \cdot 1 + A \cdot C + B \cdot A + B \cdot C$$

$$\Rightarrow A(1 + \underbrace{C+B}_x) + B \cdot C.$$

$$\Rightarrow A + B \cdot C. = R.H.S \quad \text{+ hence Proved.}$$

Ques Prove $A + \bar{A}B = A + B.$

Solⁿ L.H.S = $A + \bar{A}B.$

$$= A + \bar{A} \cdot B.$$

$$\text{So, } \cancel{(A + \bar{A})} \cdot \cancel{(B + \bar{B})} \cdot (A + B) \cdot (A + \bar{A}).$$

$$\Rightarrow A + B = \underline{R.H.S}$$

$$\underline{\bar{A} + AB}$$

$$AB$$

$$\underline{A + \bar{A}B}$$

$$A + B$$

$$\underline{A \cdot BC + A \cdot \bar{B}C \cdot D.}$$

$$A(BC + \bar{B}C \cdot D)$$

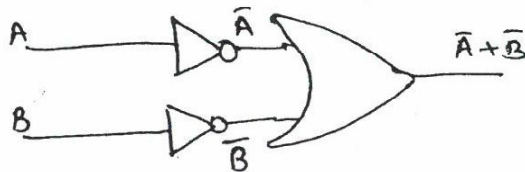
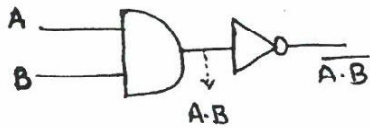
$$A \cdot (B + D).$$

De Morgan's law.

$$\overline{A+B} = \bar{A} \cdot \bar{B}$$

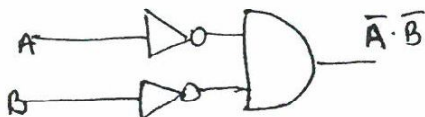
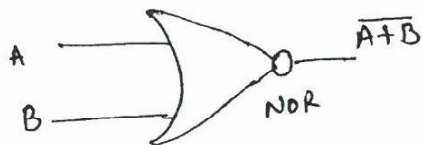
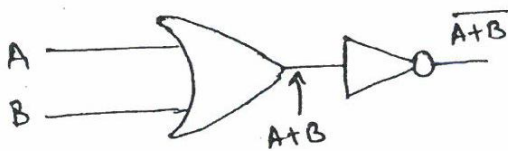
$$\overline{A \cdot B} = \bar{A} + \bar{B}$$

NAND GATE. (NOT of AND)



AB	$A \cdot B$	$\overline{A \cdot B}$
0 0	0	1
0 1	0	1
1 0	0	1
1 1	1	0

NOR GATE (NOT of OR).



AB	$A+B$	$\overline{A+B}$
0 0	0	1
0 1	1	0
1 0	1	0
1 1	1	0

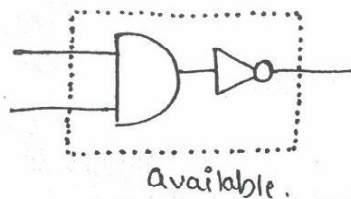
NOTE :- NAND Gate and NOR Gate are called Universal gates.

Universal / functionally Complete.

A logic circuit is said to be universal if any boolean function can be realised with instances of it.

Test :- Realise basic gates.

1. > Prove NAND gate is universal.

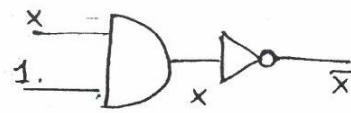
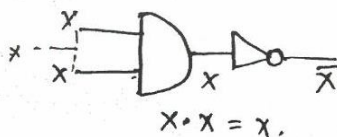


NOT Requirement



$$x \cdot x = x$$

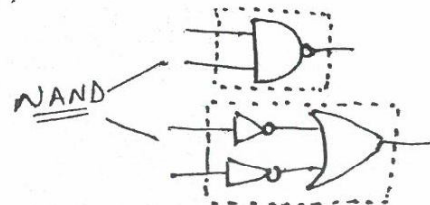
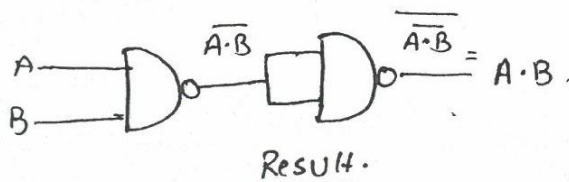
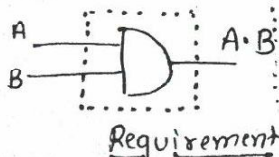
$$x \cdot 1 = x$$



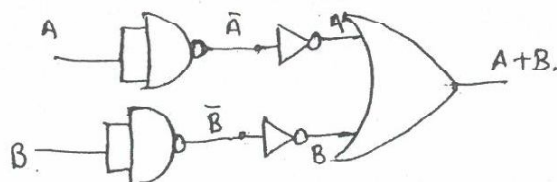
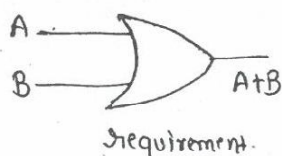
$$x \cdot 1 = x$$

NAND invertors.

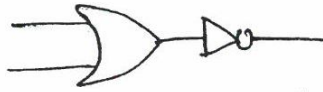
AND Requirement



OR GATE Requirement :-

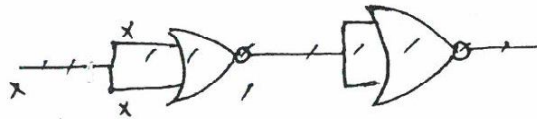
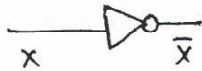


NOR GATE is Universal:-

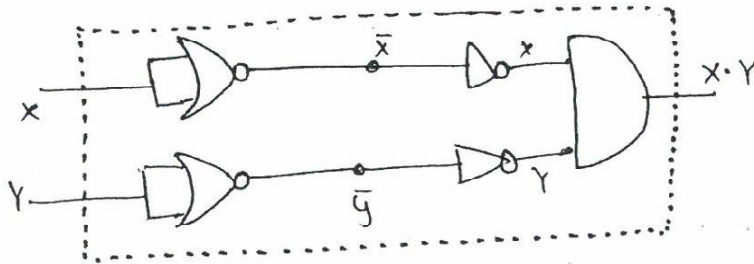
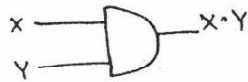


$$X + X = X, \quad X + 0 = X.$$

NOT gate:-



AND Gate.

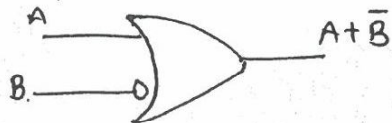


Ques Prove $A + \bar{B}$ universal

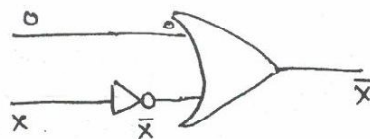
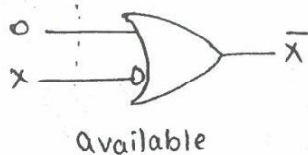
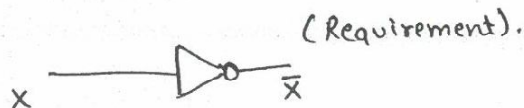
Assume V_{cc} and Ground available.

$\downarrow$ $5V (1)$ $\downarrow$ $0V (0)$.

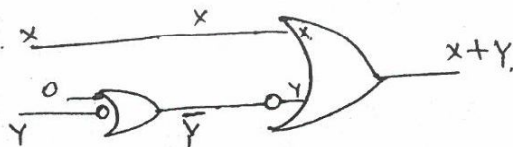
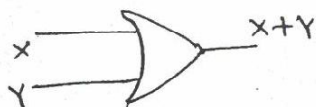
Solⁿ



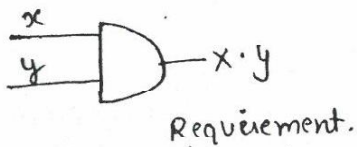
NOT



OR

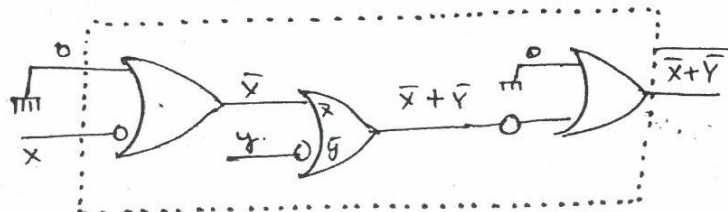
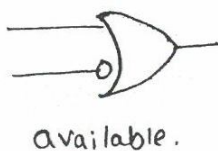


AND



$$\overline{X \cdot Y} = \bar{X} + \bar{Y}$$

$$\overline{\bar{X} + \bar{Y}} = X \cdot Y$$

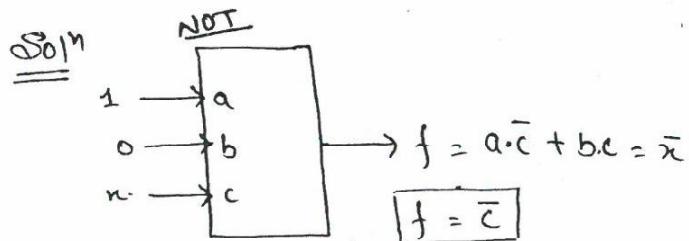


$\{A + \bar{B}, 0\}$ is universal.

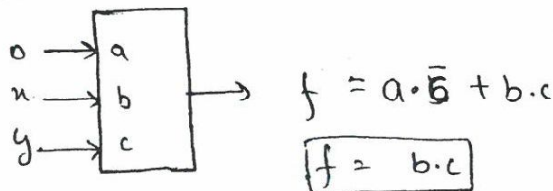
Ques Which of the following is Total function.

- a) $\{A \cdot B\}$
- b) $\{A+B, 0\}$
- ✓ c) $\{A \cdot \bar{B}, 1\}$
- d) $\{A+B, 1\}$

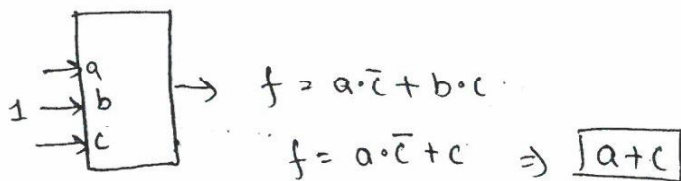
Ques Prove $f(a, b, c) = a \cdot \bar{c} + bc$ universal.



AND



OR



Ques Practice

Consider the operations.

$$f(x, y, z) = x y z + x \bar{y} + \bar{y} \bar{z}$$

$$g(x, y, z) = \bar{x} y z + \bar{x} y \bar{z} + x y$$

Which of the following is functionally complete.